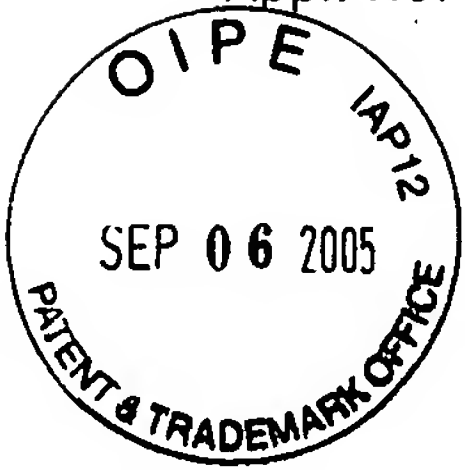


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/609,311
Filing Date June 26, 2003
Inventor Donald L. Yates
Assignee Micron Technologies, Inc.
Group Art Unit 1736
Examiner Nicole M. Barreca
Attorney's Docket No. MI22-2236
Title: Semiconductor Processing Patterning Methods (as amended)

RESPONSE TO MAY 6, 2005 OFFICE ACTION

To: Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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